

# **DATA SHEET**

**BFG520W**  
**BFG520W/X; BFG520W/XR**  
**NPN 9 GHz wideband transistor**

Product specification  
File under Discrete Semiconductors, SC14

August 1995

**Philips Semiconductors**



**PHILIPS**

**NPN 9 GHz wideband transistor**

**BFG520W**  
**BFG520W/X; BFG520W/XR**

**FEATURES**

- High power gain
- Low noise figure
- High transition frequency
- Gold metallization ensures excellent reliability.

**APPLICATIONS**

They are intended for applications in the RF front end, in wideband applications in the GHz range such as analog and digital cellular telephones, cordless telephones (CT2, CT3, PCN, DECT, etc.), radar detectors, pagers, satellite television tuners (SATV) and repeater amplifiers in fibre-optic systems.

**DESCRIPTION**

NPN silicon planar epitaxial transistors in plastic, 4-pin dual-emitter SOT343 and SOT343R packages.

**MARKING**

TYPE NUMBER	CODE
BFG520W	N3
BFG520W/X	N4
BFG520W/XR	N5

**PINNING**

PIN	DESCRIPTION
<b>BFG520W (see Fig.1)</b>	
1	collector
2	base
3	emitter
4	emitter
<b>BFG520W/X (see Fig.1)</b>	
1	collector
2	emitter
3	base
4	emitter
<b>BFG520W/XR (see Fig.2)</b>	
1	collector
2	emitter
3	base
4	emitter

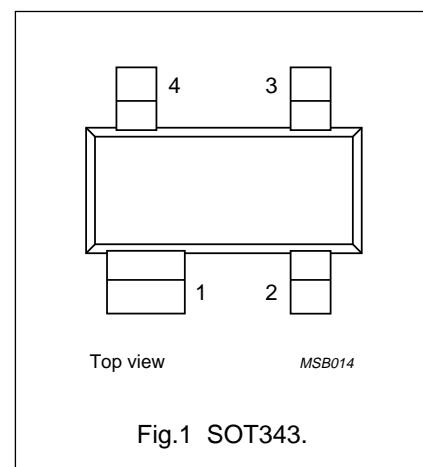


Fig.1 SOT343.

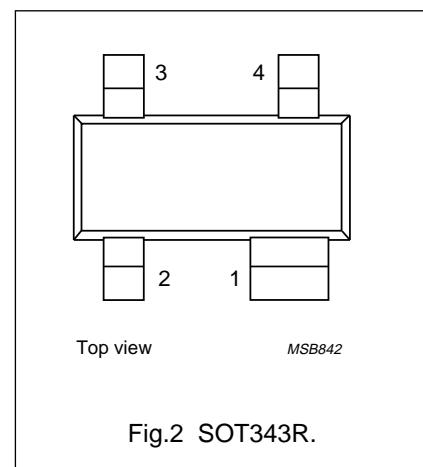


Fig.2 SOT343R.

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	—	—	20	V
$V_{CEO}$	collector-emitter voltage	open base	—	—	15	V
$I_C$	collector current (DC)		—	—	70	mA
$P_{tot}$	total power dissipation	up to $T_s = 85^\circ\text{C}$	—	—	500	mW
$h_{FE}$	DC current gain	$I_C = 20 \text{ mA}; V_{CE} = 6 \text{ V}$	60	120	250	
$C_{re}$	feedback capacitance	$I_C = 0; V_{CB} = 6 \text{ V}; f = 1 \text{ MHz}$	—	0.35	—	pF
$f_T$	transition frequency	$I_C = 20 \text{ mA}; V_{CE} = 6 \text{ V}; f = 1 \text{ GHz}; T_{amb} = 25^\circ\text{C}$	—	9	—	GHz
$G_{UM}$	maximum unilateral power gain	$I_C = 20 \text{ mA}; V_{CE} = 6 \text{ V}; f = 900 \text{ MHz}; T_{amb} = 25^\circ\text{C}$	—	17	—	dB
$ s_{21} ^2$	insertion power gain	$I_C = 20 \text{ mA}; V_{CE} = 6 \text{ V}; f = 900 \text{ MHz}; T_{amb} = 25^\circ\text{C}$	16	17	—	dB
F	noise figure	$\Gamma_s = \Gamma_{opt}; I_C = 5 \text{ mA}; V_{CE} = 6 \text{ V}; f = 900 \text{ MHz}$	—	1.1	1.6	dB

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**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

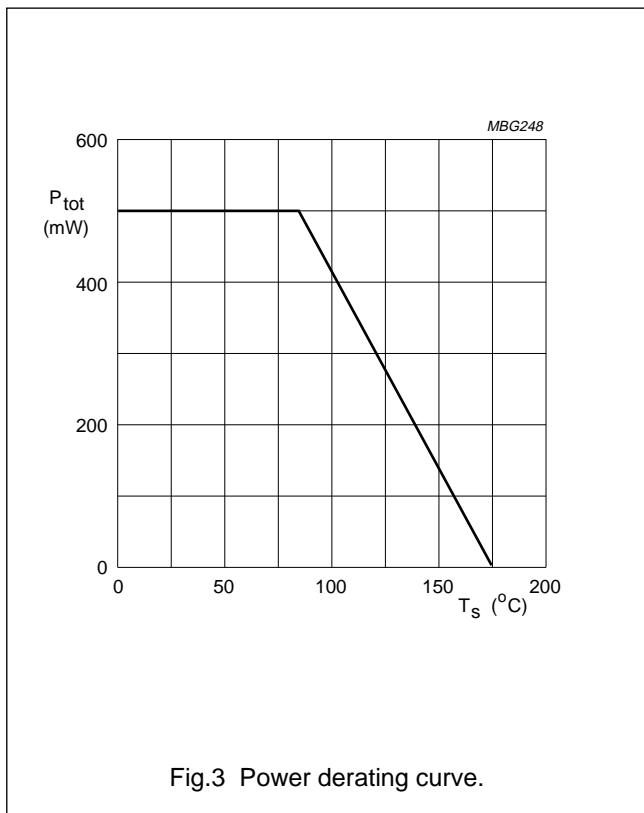
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	20	V
$V_{CEO}$	collector-emitter voltage	open base	–	15	V
$V_{EBO}$	emitter-base voltage	open collector	–	2.5	V
$I_C$	collector current (DC)		–	70	mA
$P_{tot}$	total power dissipation	up to $T_s = 85^\circ\text{C}$ ; see Fig.3; note 1	–	500	mW
$T_{stg}$	storage temperature		–65	+150	$^\circ\text{C}$
$T_j$	junction temperature		–	175	$^\circ\text{C}$

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th j-s}$	thermal resistance from junction to soldering point	up to $T_s = 85^\circ\text{C}$ ; note 1	180	K/W

**Note to the "Limiting values" and "Thermal characteristics"**

1.  $T_s$  is the temperature at the soldering point of the collector pin.



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**CHARACTERISTICS**

$T_j = 25^\circ\text{C}$  (unless otherwise specified).

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(\text{BR})\text{CBO}}$	collector-base breakdown voltage	open emitter; $I_C = 0.01 \text{ mA}$ ; $I_E = 0$	–	–	20	V
$V_{(\text{BR})\text{CEO}}$	collector-emitter breakdown voltage	open base; $I_C = 10 \text{ mA}$ ; $I_B = 0$	–	–	15	V
$V_{(\text{BR})\text{EBO}}$	emitter-base breakdown voltage	open collector; $I_E = 0.01 \text{ mA}$ ; $I_C = 0$	–	–	2.5	V
$I_{\text{CBO}}$	collector cut-off current	open emitter; $V_{\text{CB}} = 6 \text{ V}$ ; $I_E = 0$	–	–	50	nA
$h_{\text{FE}}$	DC current gain	$I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$	60	120	250	
$C_{\text{re}}$	feedback capacitance	$I_C = 0$ ; $V_{\text{CB}} = 6 \text{ V}$ ; $f = 1 \text{ MHz}$	–	0.35	–	pF
$f_T$	transition frequency	$I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 1 \text{ GHz}$ ; $T_{\text{amb}} = 25^\circ\text{C}$	–	9	–	GHz
$G_{\text{UM}}$	maximum unilateral power gain; note 1	$I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 900 \text{ MHz}$ ; $T_{\text{amb}} = 25^\circ\text{C}$	–	17	–	dB
		$I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 2 \text{ GHz}$ ; $T_{\text{amb}} = 25^\circ\text{C}$	–	11	–	dB
$ \mathbf{s}_{21} ^2$	insertion power gain	$I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 900 \text{ MHz}$ ; $T_{\text{amb}} = 25^\circ\text{C}$	16	17	–	dB
F	noise figure	$\Gamma_s = \Gamma_{\text{opt}}$ ; $I_C = 5 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 900 \text{ MHz}$	–	1.1	1.6	dB
		$\Gamma_s = \Gamma_{\text{opt}}$ ; $I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 900 \text{ MHz}$	–	1.6	2.1	dB
		$\Gamma_s = \Gamma_{\text{opt}}$ ; $I_C = 5 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 2 \text{ GHz}$	–	1.85	–	dB
$P_{L1}$	output power at 1 dB gain compression	$I_C = 20 \text{ mA}$ ; $V_{\text{CE}} = 6 \text{ V}$ ; $f = 900 \text{ MHz}$ ; $R_L = 50 \Omega$ ; $T_{\text{amb}} = 25^\circ\text{C}$	–	17	–	dBm
ITO	third order intercept point	note 2	–	26	–	dBm
$V_o$	output voltage	note 3	–	275	–	mV
$d_2$	second order intermodulation distortion	note 4	–	–50	–	dB

**Notes**

- $G_{\text{UM}}$  is the maximum unilateral power gain, assuming  $s_{12}$  is zero.  $G_{\text{UM}} = 10 \log \frac{|\mathbf{s}_{21}|^2}{(1 - |\mathbf{s}_{11}|^2)(1 - |\mathbf{s}_{22}|^2)}$  dB.
- $I_C = 20 \text{ mA}$ ;  $V_{\text{CE}} = 6 \text{ V}$ ;  $R_L = 50 \Omega$ ;  $T_{\text{amb}} = 25^\circ\text{C}$ ;  
 $f_p = 900 \text{ MHz}$ ;  $f_q = 902 \text{ MHz}$ ; measured at  $f_{(2p-q)} = 898 \text{ MHz}$  and  $f_{(2q-p)} = 904 \text{ MHz}$ .
- $d_{\text{im}} = -60 \text{ dB}$  (DIN45004B);  $I_C = 20 \text{ mA}$ ;  $V_{\text{CE}} = 6 \text{ V}$ ;  $V_p = V_o$ ;  $V_q = V_o - 6 \text{ dB}$ ;  $V_r = V_o - 6 \text{ dB}$ ;  $R_L = 75 \Omega$ ;  
 $f_p = 795.25 \text{ MHz}$ ;  $f_q = 803.25 \text{ MHz}$ ;  $f_r = 805.25 \text{ MHz}$ ; measured at  $f_{(p+q-r)} = 793.25 \text{ MHz}$ .
- $I_C = 20 \text{ mA}$ ;  $V_{\text{CE}} = 6 \text{ V}$ ;  $V_o = 75 \text{ mV}$ ;  $R_L = 75 \Omega$ ;  $T_{\text{amb}} = 25^\circ\text{C}$ ;  
 $f_p = 250 \text{ MHz}$ ;  $f_q = 560 \text{ MHz}$ ; measured at  $f_{(p+q)} = 810 \text{ MHz}$ .

## NPN 9 GHz wideband transistor

BFG520W

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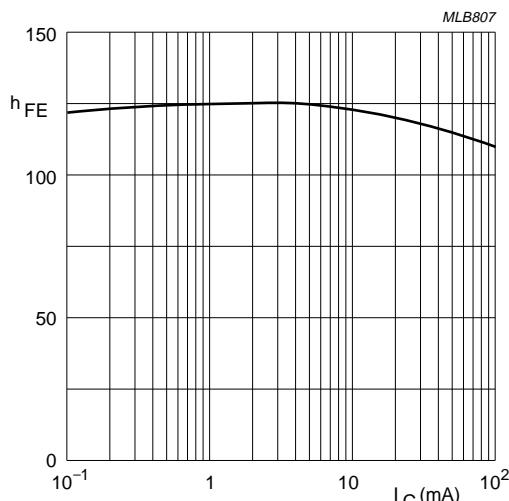
 $V_{CE} = 6$  V.

Fig.4 DC current gain as a function of collector current; typical values.

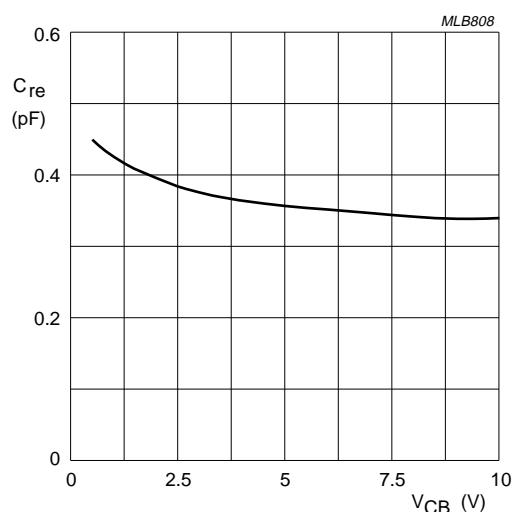
 $I_C = 0$ ;  $f = 1$  MHz.

Fig.5 Feedback capacitance as a function of collector-base voltage; typical values.

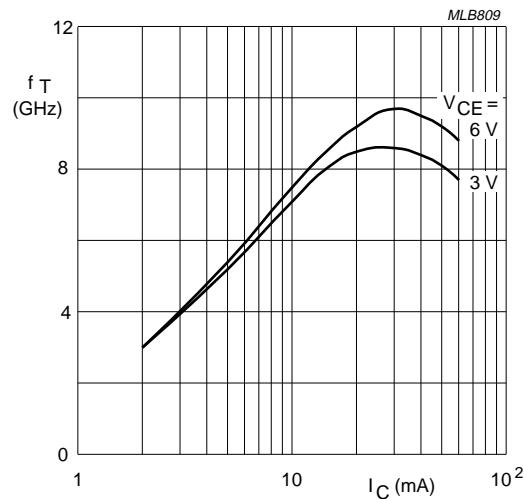
 $f = 1$  GHz;  $T_{amb} = 25$  °C.

Fig.6 Transition frequency as a function of collector current; typical values.

## NPN 9 GHz wideband transistor

BFG520W

BFG520W/X; BFG520W/XR

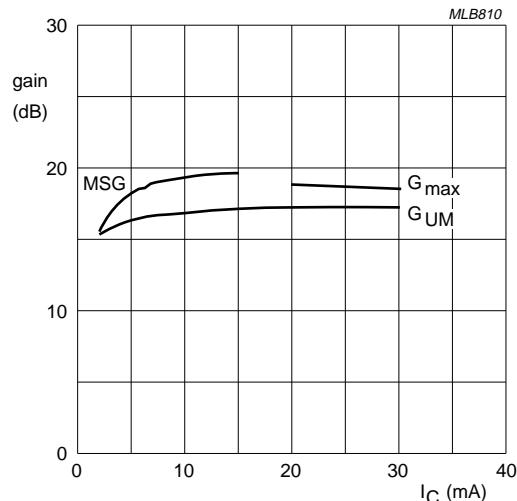
 $f = 900$  MHz;  $V_{CE} = 6$  V.

Fig.7 Gain as a function of collector current; typical values.

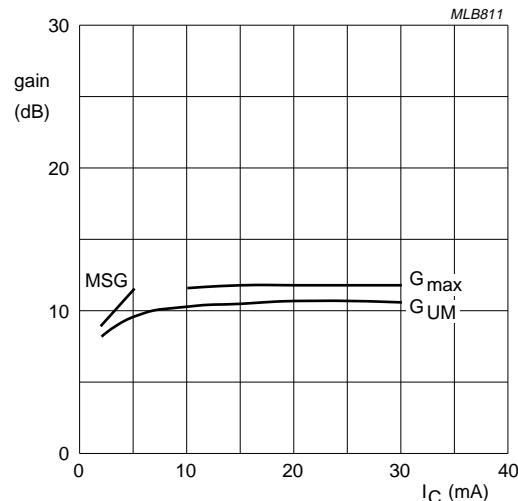
 $f = 2$  GHz;  $V_{CE} = 6$  V.

Fig.8 Gain as a function of collector current; typical values.

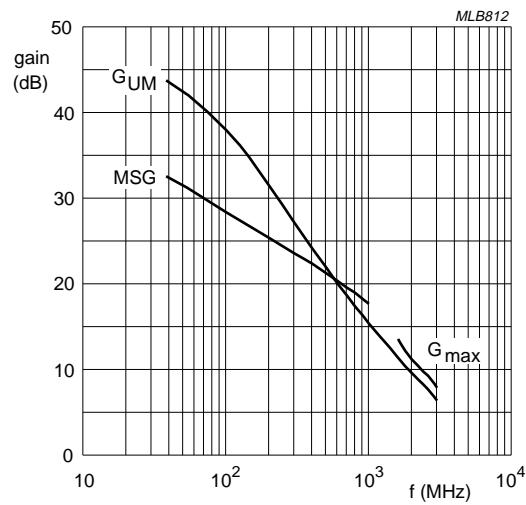
 $I_C = 5$  mA;  $V_{CE} = 6$  V.

Fig.9 Gain as a function of frequency; typical values.

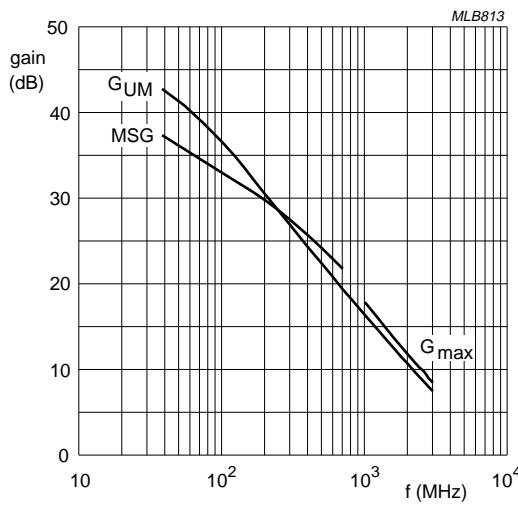
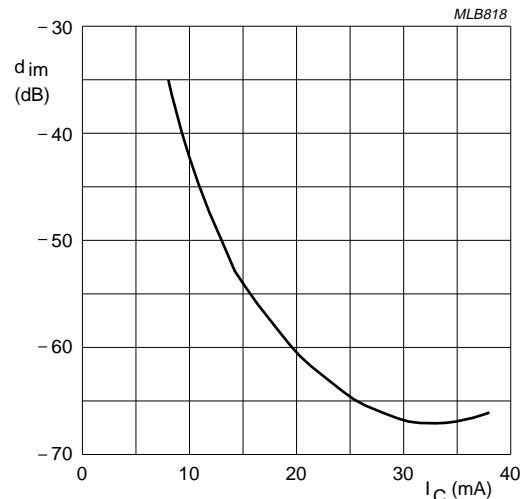
 $I_C = 20$  mA;  $V_{CE} = 6$  V.

Fig.10 Gain as a function of frequency; typical values.

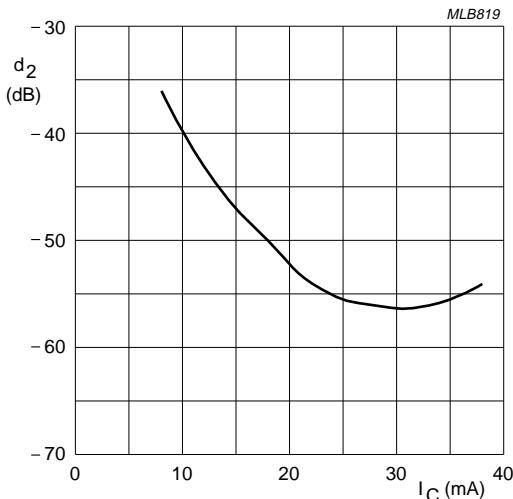
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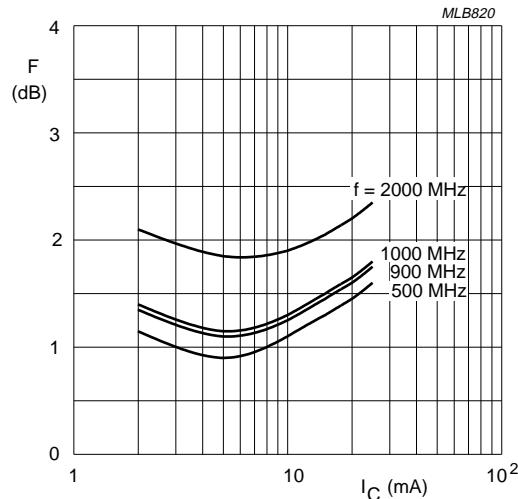
$V_o = 275$  mV;  $f_{(p+q-r)} = 793.25$  MHz;  $V_{CE} = 6$  V;  $R_L = 75 \Omega$ ;  $T_{amb} = 25^\circ\text{C}$ .

Fig.11 Intermodulation distortion as a function of collector current; typical values.



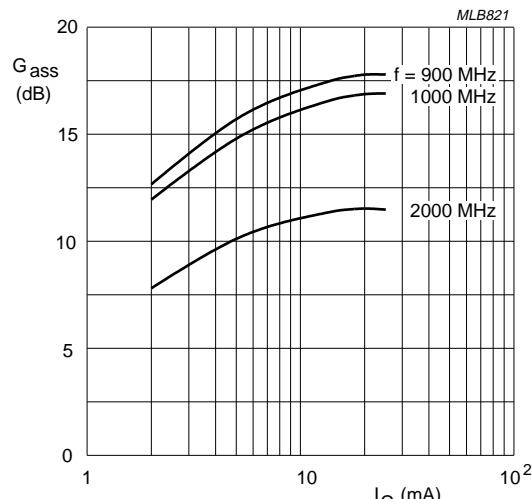
$V_o = 75$  mV;  $f_{(p+q)} = 810$  MHz;  $V_{CE} = 6$  V;  $R_L = 75 \Omega$ ;  $T_{amb} = 25^\circ\text{C}$ .

Fig.12 Second order intermodulation distortion as a function of collector current; typical values.



$V_{CE} = 6$  V.

Fig.13 Minimum noise figure as a function of collector current; typical values.



$V_{CE} = 6$  V.

Fig.14 Associated available gain as a function of collector current; typical values.

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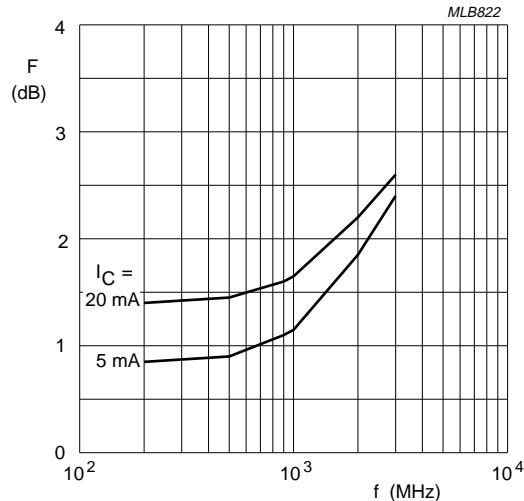
 $V_{CE} = 6 \text{ V.}$ 

Fig.15 Minimum noise figure as a function of frequency; typical values.

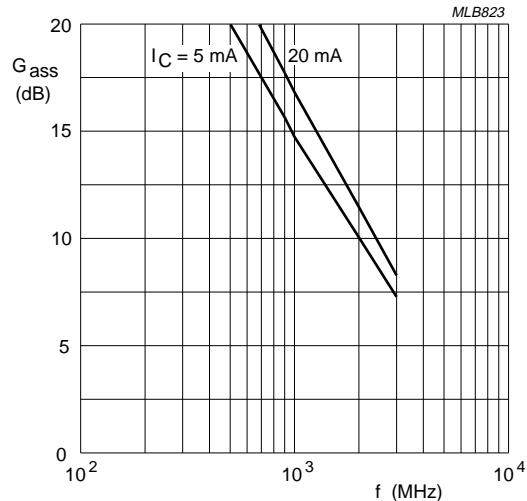
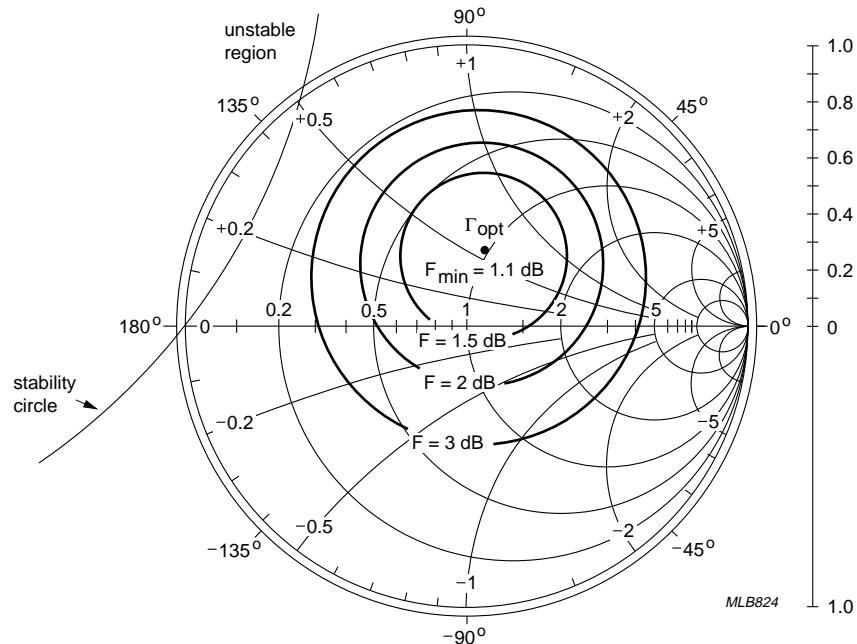
 $V_{CE} = 6 \text{ V.}$ 

Fig.16 Associated available gain as a function of frequency; typical values.

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$f = 900 \text{ MHz}; V_{CE} = 6 \text{ V}; I_C = 5 \text{ mA}; Z_0 = 50 \Omega$ .

Fig.17 Common emitter noise figure circles; typical values.

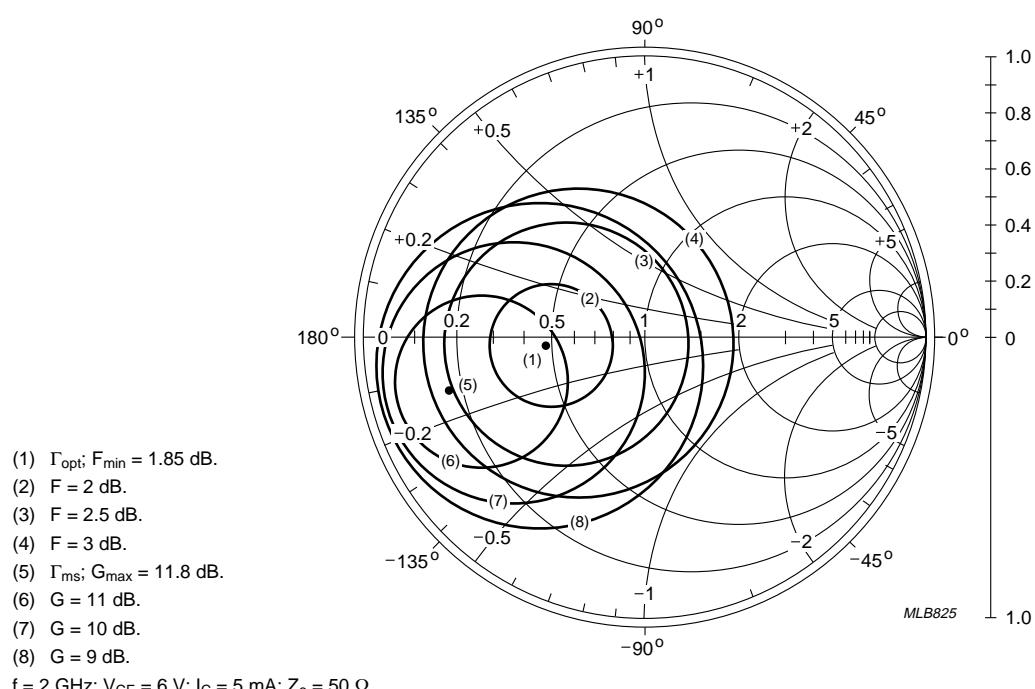
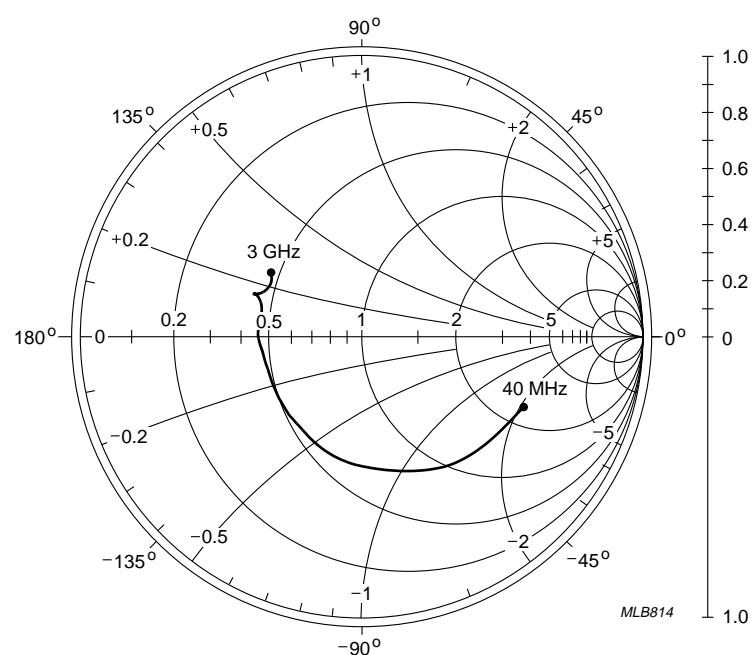
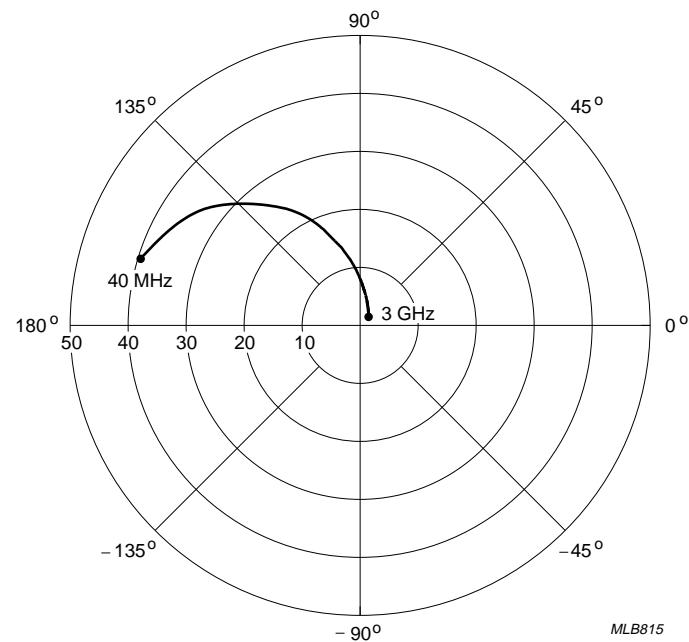
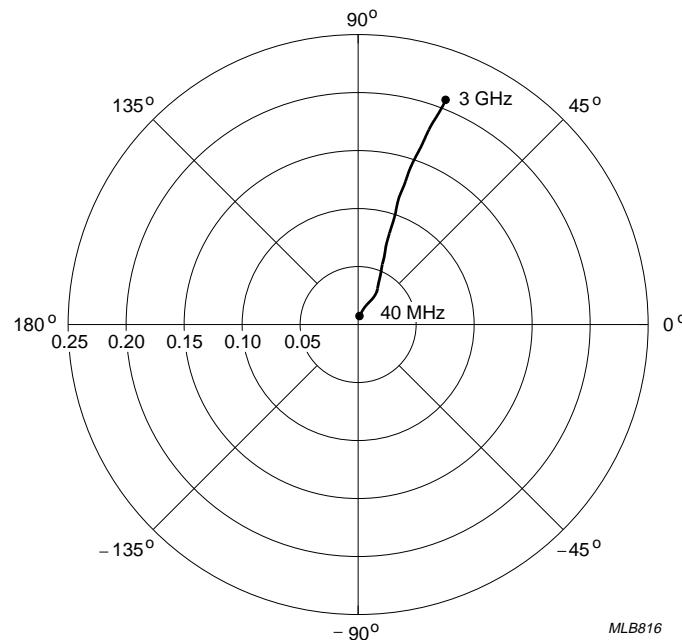
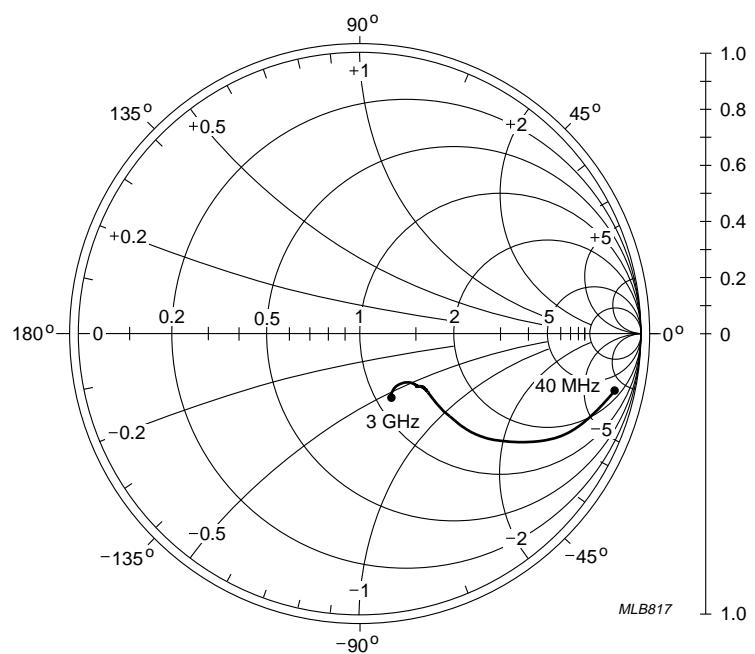


Fig.18 Common emitter noise figure circles; typical values.

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BFG520W/X; BFG520W/XR $V_{CE} = 6 \text{ V}; I_C = 20 \text{ mA}; Z_0 = 50 \Omega$ .Fig.19 Common emitter input reflection coefficient ( $s_{11}$ ); typical values. $V_{CE} = 6 \text{ V}; I_C = 20 \text{ mA}$ .Fig.20 Common emitter forward transmission coefficient ( $s_{21}$ ); typical values.

## NPN 9 GHz wideband transistor

BFG520W  
BFG520W/X; BFG520W/XR $V_{CE} = 6 \text{ V}; I_C = 20 \text{ mA}.$ Fig.21 Common emitter reverse transmission coefficient ( $s_{12}$ ); typical values. $V_{CE} = 6 \text{ V}; I_C = 20 \text{ mA}; Z_0 = 50 \Omega.$ Fig.22 Common emitter output reflection coefficient ( $s_{22}$ ); typical values.

## NPN 9 GHz wideband transistor

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## SPICE parameters for the BFG520W crystal

SEQUENCE No.	PARAMETER	VALUE	UNIT
1	IS	1.016	fA
2	BF	220.1	–
3	NF	1.000	–
4	VAF	48.06	V
5	IKF	510	mA
6	ISE	283	fA
7	NE	2.035	–
8	BR	100.7	–
9	NR	0.988	–
10	VAR	1.692	V
11	IKR	2.352	mA
12	ISC	24.48	aA
13	NC	1.022	–
14	RB	10.00	Ω
15	IRB	1.000	μA
16	RBM	10.00	Ω
17	RE	775.3	mΩ
18	RC	2.210	Ω
19 <sup>(1)</sup>	XTB	0.000	–
20 <sup>(1)</sup>	EG	1.110	eV
21 <sup>(1)</sup>	XTI	3.000	–
22	CJE	1.245	pF
23	VJE	600.0	mV
24	MJE	0.258	–
25	TF	8.616	ps
26	XTF	6.788	–
27	VTF	1.414	V
28	ITF	110.3	mA
29	PTF	45.01	deg
30	CJC	447.6	fF
31	VJC	189.2	mV
32	MJC	0.070	–
33	XCJC	0.130	–
34	TR	543.7	ps
35 <sup>(1)</sup>	CJS	0.000	F

SEQUENCE No.	PARAMETER	VALUE	UNIT
36 <sup>(1)</sup>	VJS	750.0	mV
37 <sup>(1)</sup>	MJS	0.000	–
38	FC	0.780	–

## Note

- These parameters have not been extracted, the default values are shown.

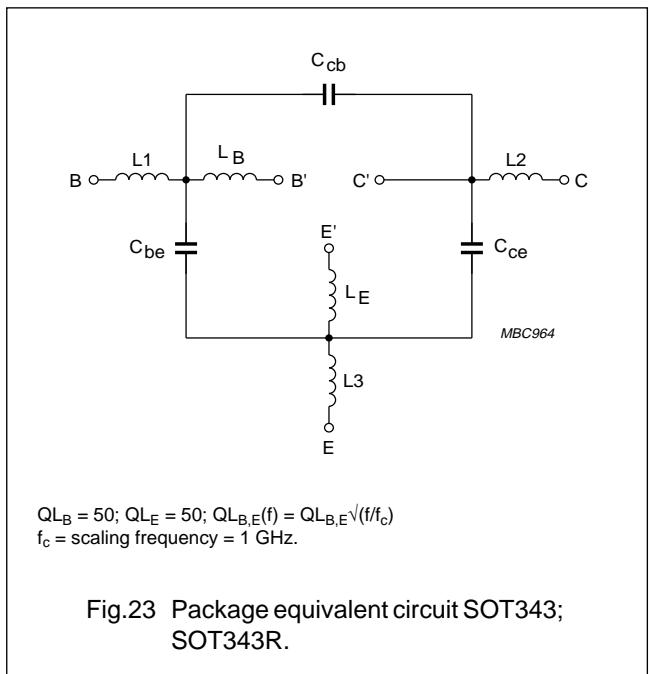


Fig.23 Package equivalent circuit SOT343;  
SOT343R.

## List of components (see Fig.23)

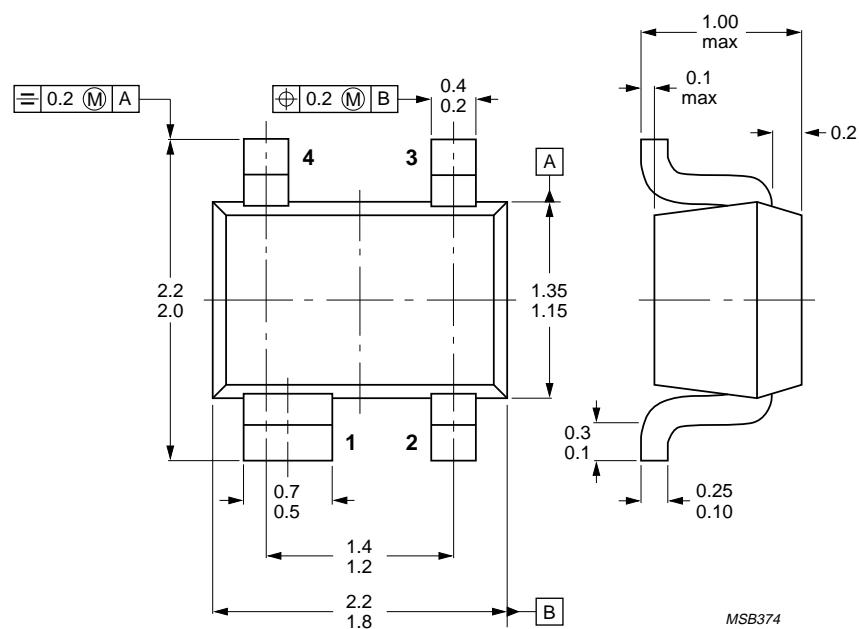
DESIGNATION	VALUE	UNIT
$C_{be}$	70	fF
$C_{cb}$	50	fF
$C_{ce}$	115	fF
$L_1$	0.34	nH
$L_2$	0.10	nH
$L_3$	0.25	nH
$L_B$	0.40	nH
$L_E$	0.40	nH

## NPN 9 GHz wideband transistor

BFG520W

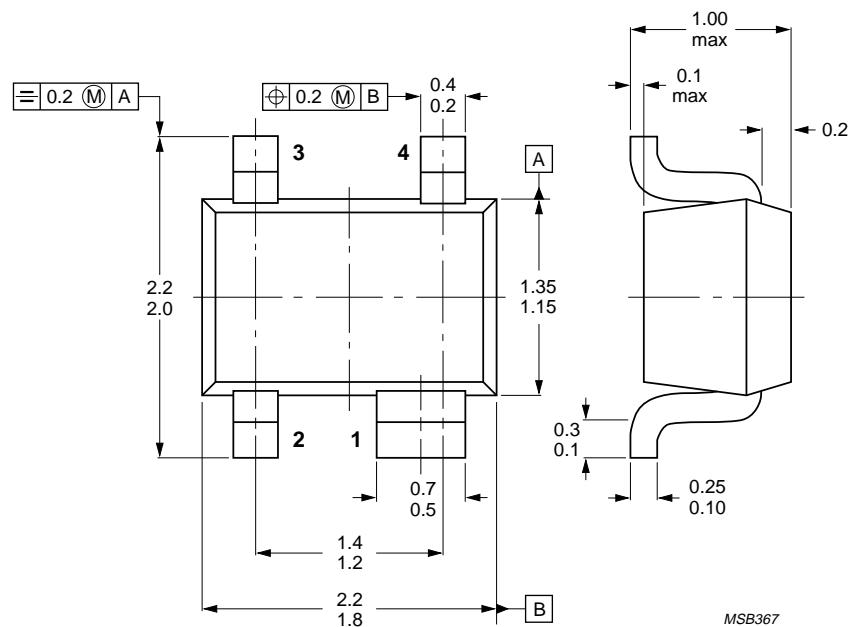
## BFG520W/X; BFG520W/XR

## PACKAGE OUTLINES



Dimensions in mm.

Fig.24 SOT343.



Dimensions in mm.

Fig.25 SOT343R.

**NPN 9 GHz wideband transistor****BFG520W**  
**BFG520W/X; BFG520W/XR****DEFINITIONS**

<b>Data Sheet Status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

**LIFE SUPPORT APPLICATIONS**

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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**NOTES**

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